

FIG. 1

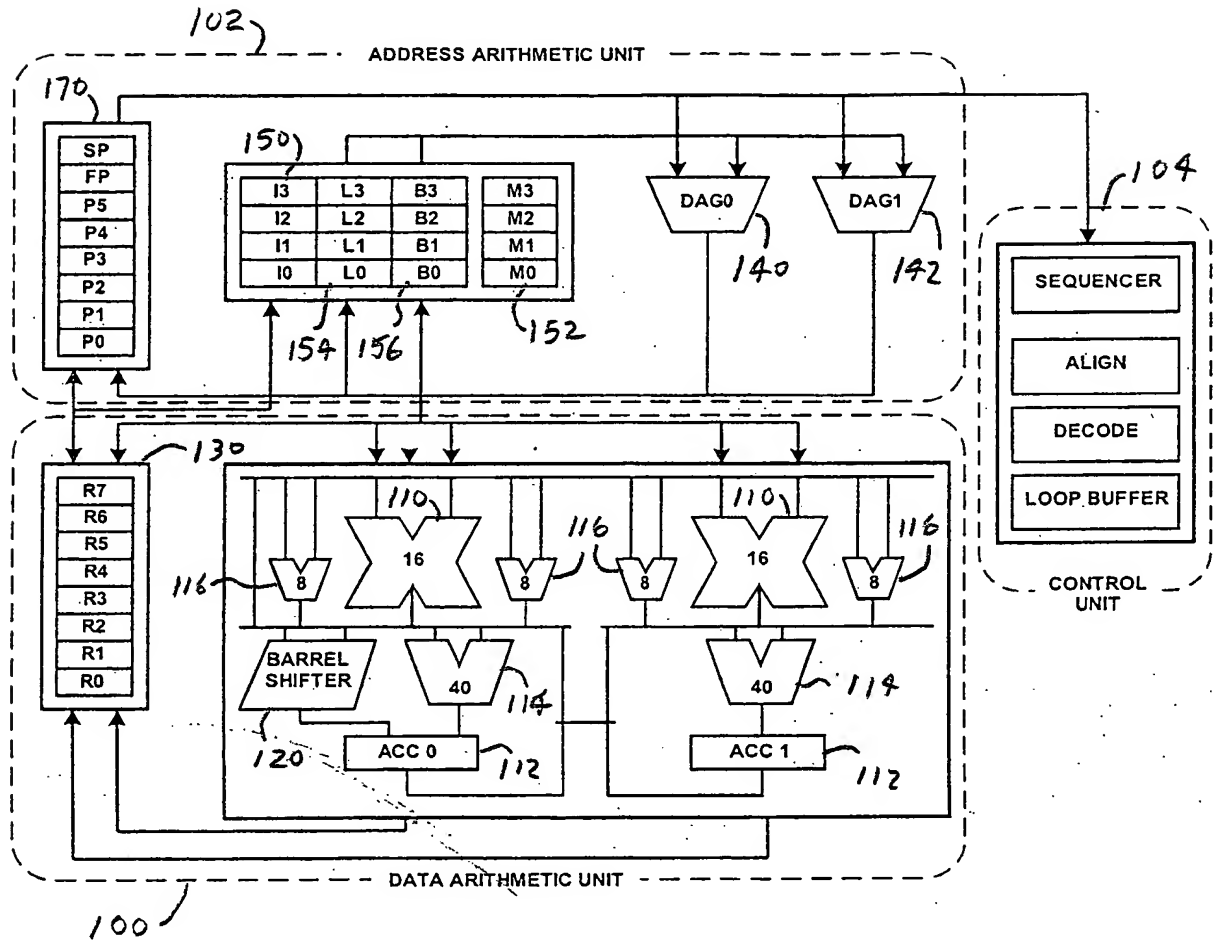


FIG. 2

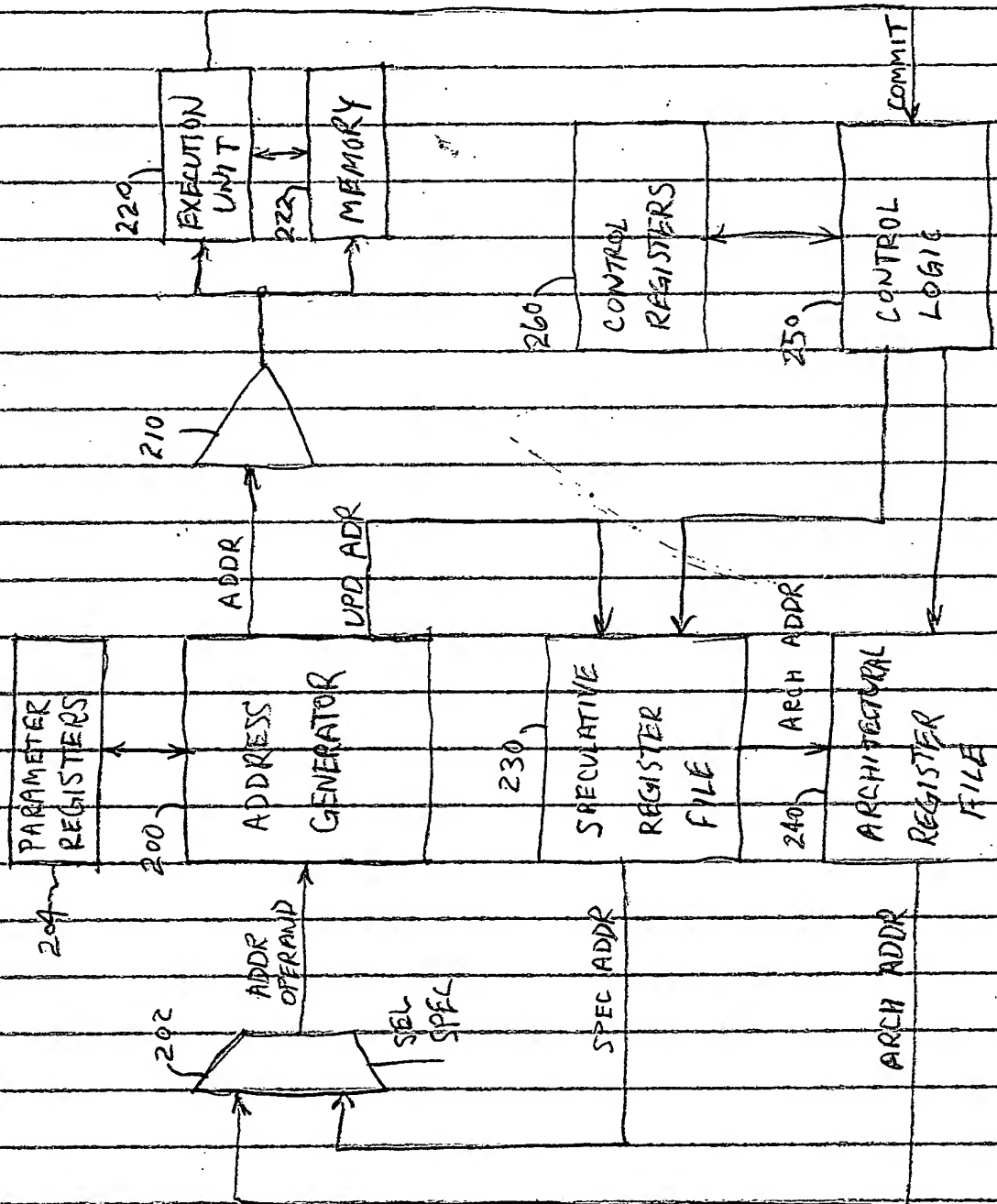


FIG. 3

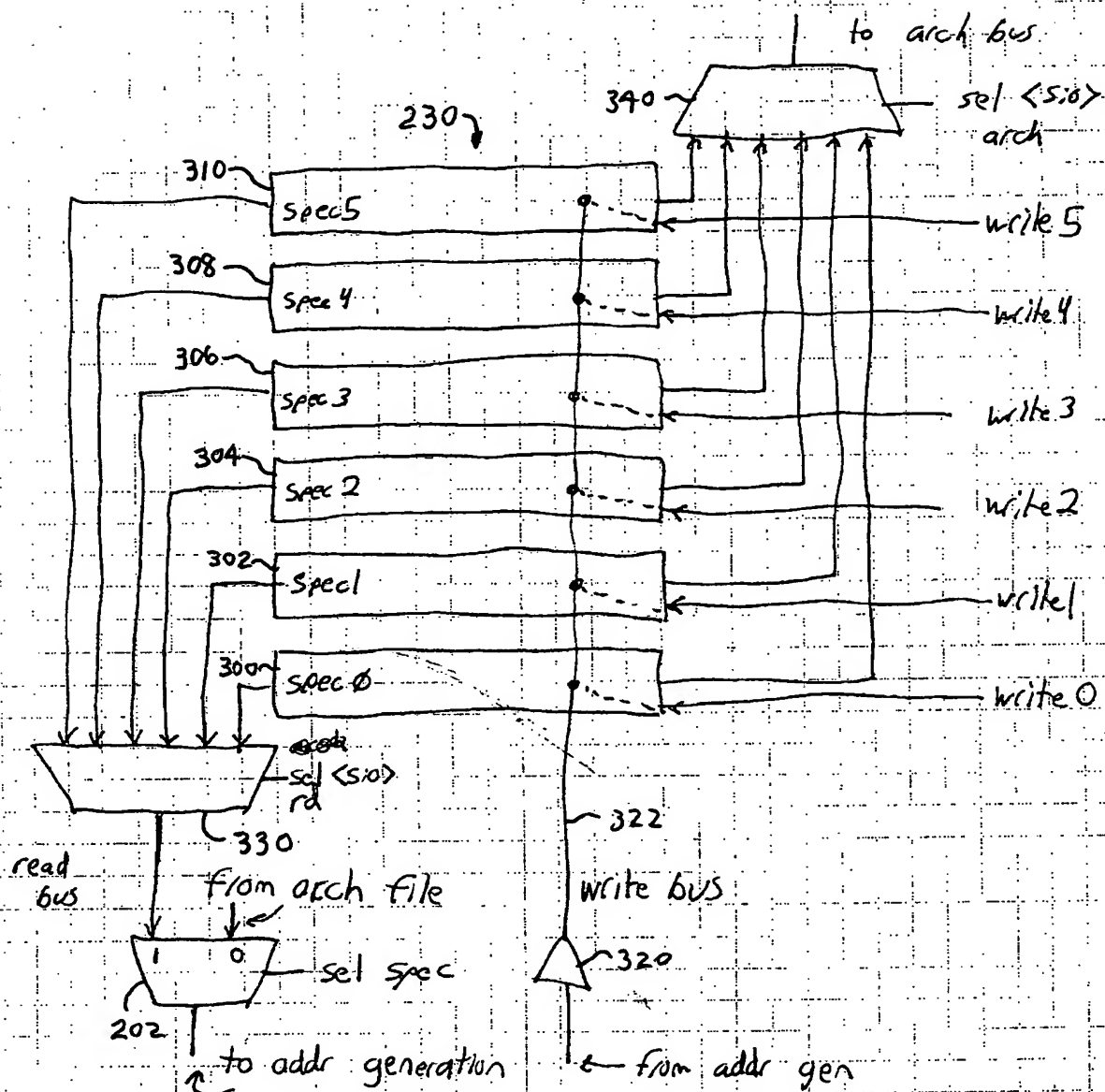


FIG. 4



FIG. 5

Example 0

A       $ro = [Io ++ Mo]$

B       $ro = [Io ++ Mo]$

C       $ro = [Io ++ Mo]$

D       $ro = [Io ++ Mo]$

E       $ro = [Io ++ Mo]$

F       $ro = [Io ++ Mo]$

G       $ro = [Io ++ Mo]$

FIG. 6

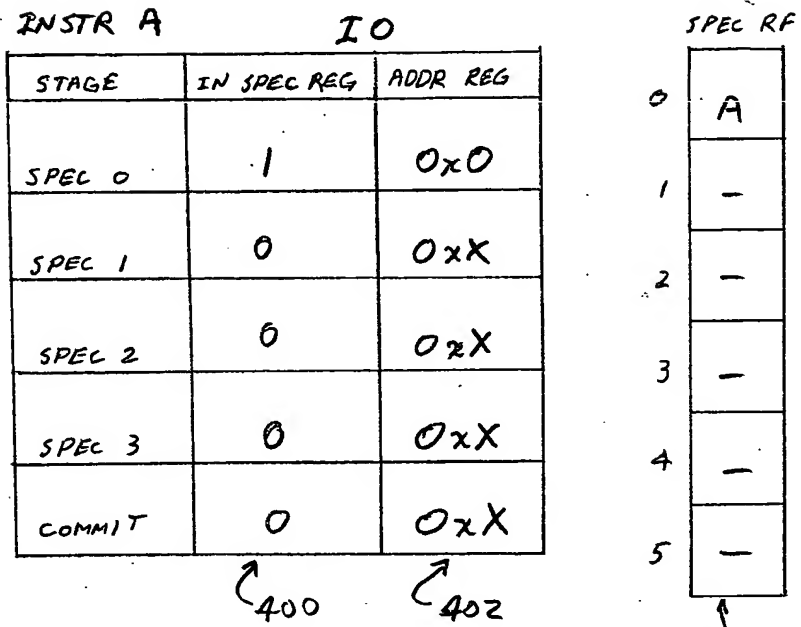


FIG. 7A

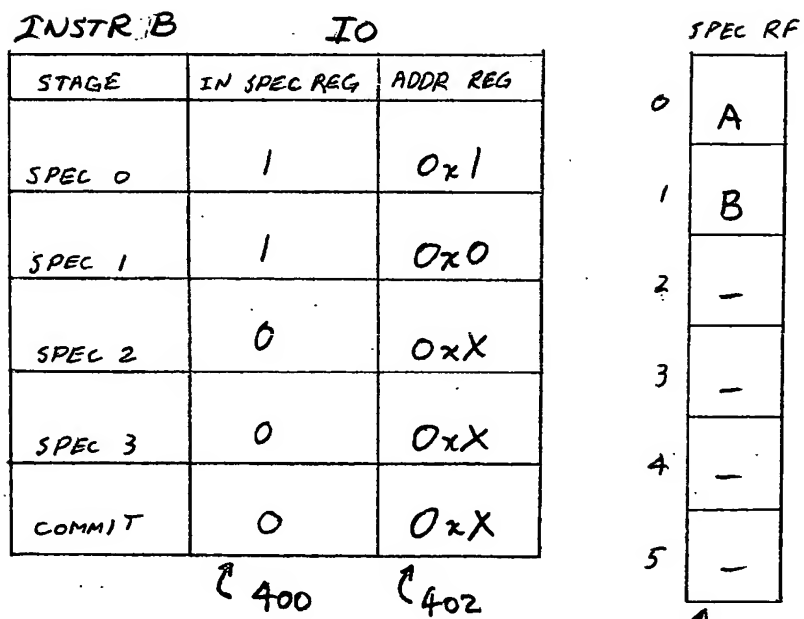


FIG. 7B

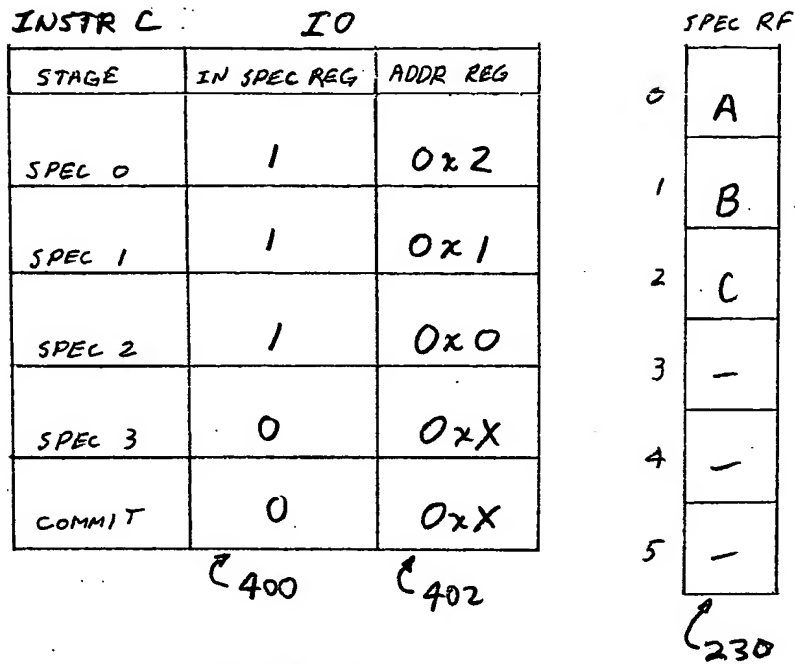


FIG. 7C

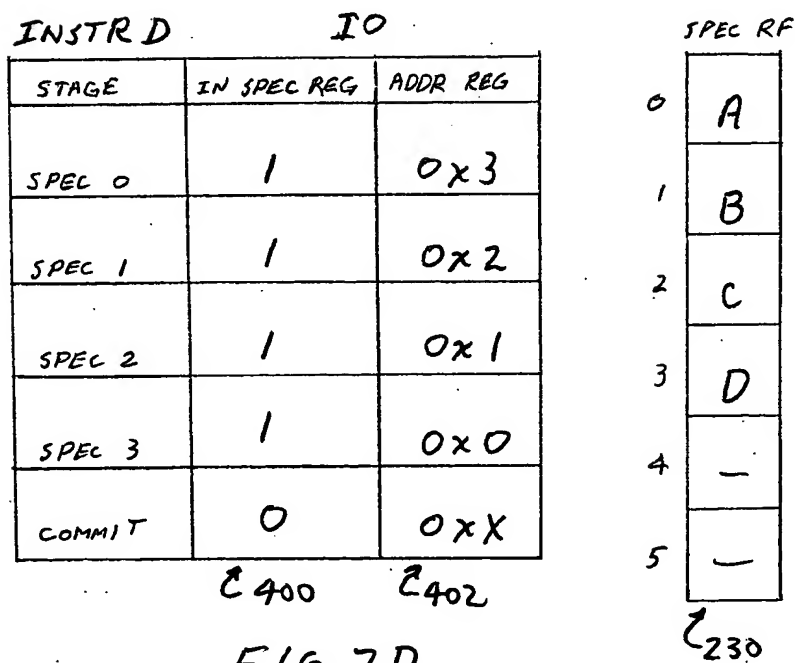
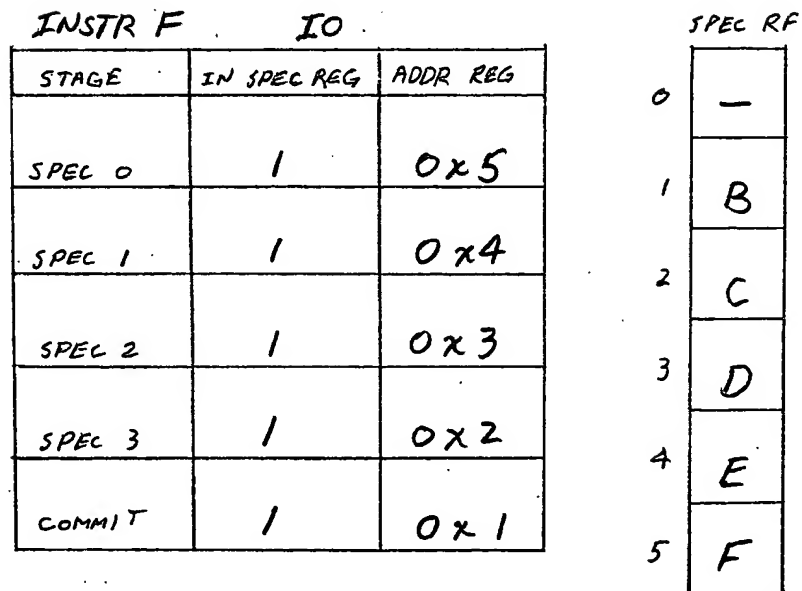
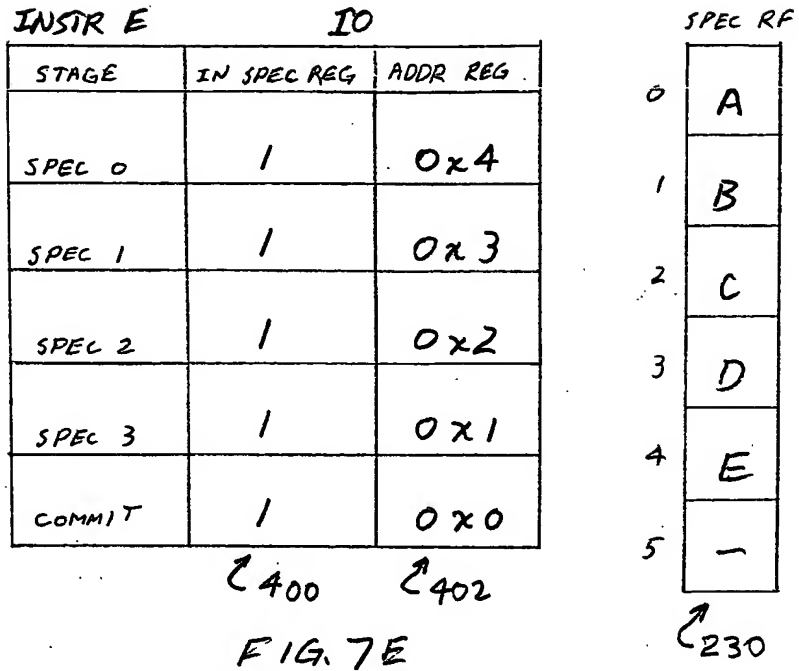


FIG. 7D





**FIG. 7F**

INSTR G IO			SPEC RF	
STAGE	IN SPEC REG	ADDR REG		
SPEC 0	1	0x0	0	G
SPEC 1	1	0x5	1	-
SPEC 2	1	0x4	2	C
SPEC 3	1	0x3	3	D
COMMIT	1	0x2	4	E
			5	F

FIG. 7G

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Example 1

A  $r_0 = [p_0++]$

B  $r_1 = [p_1++]$

C  $r_2 = [p_1++]$

D  $r_3 = [p_1++]$

E  $r_4 = [p_1++]$

F  $r_5 = [p_1++]$

G  $r_6 = [p_1++]$

FIG. 8

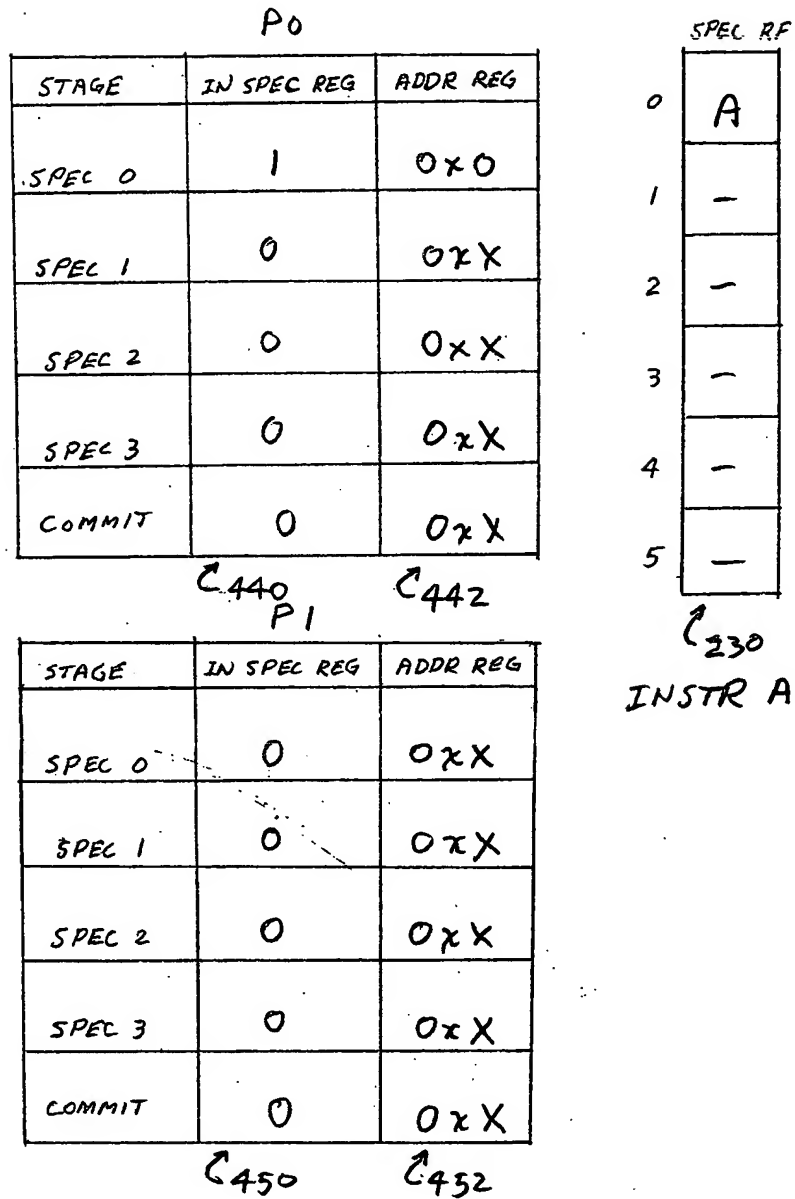
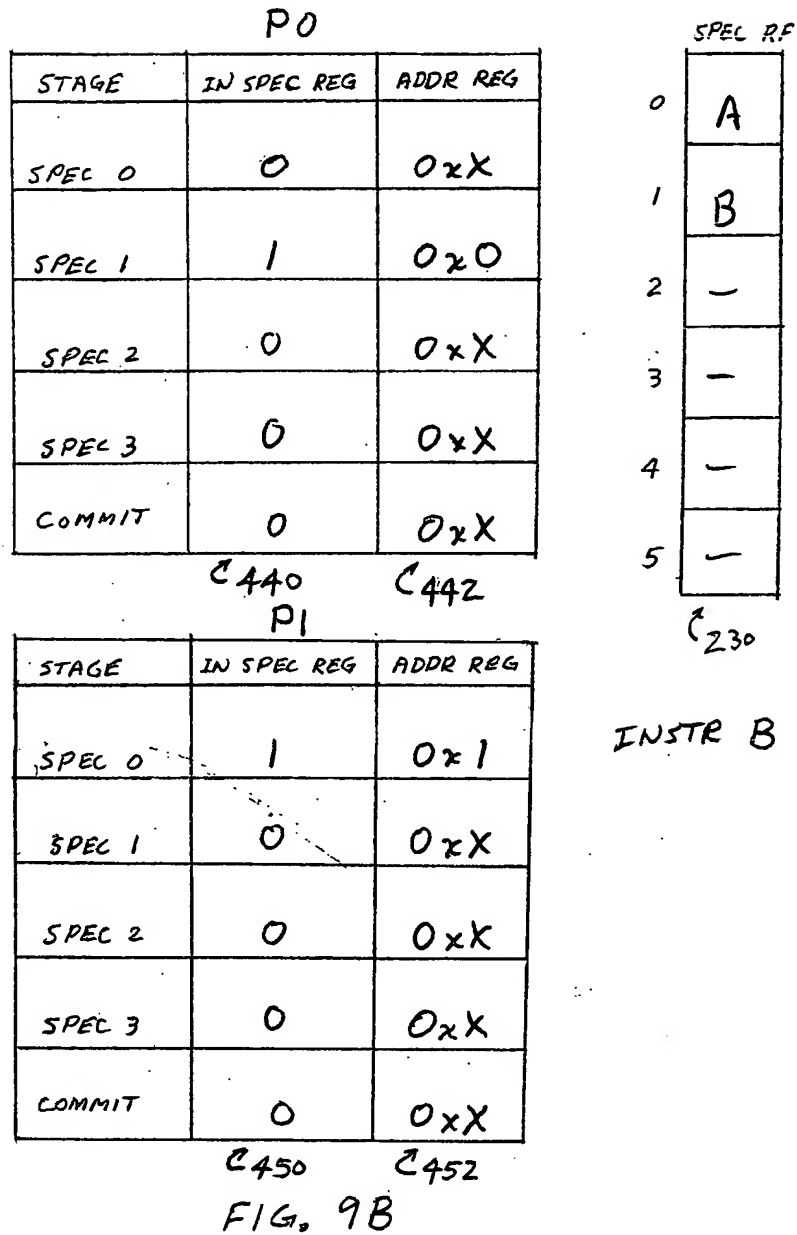
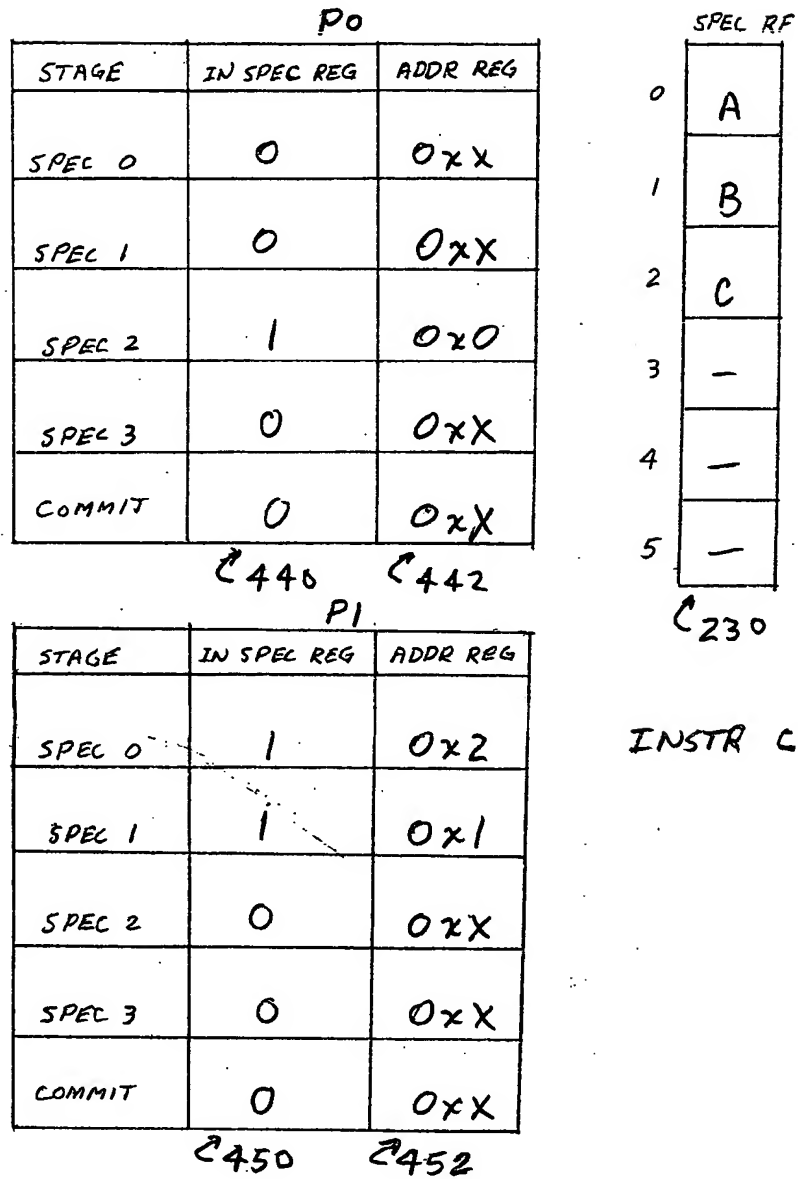


FIG. 9A





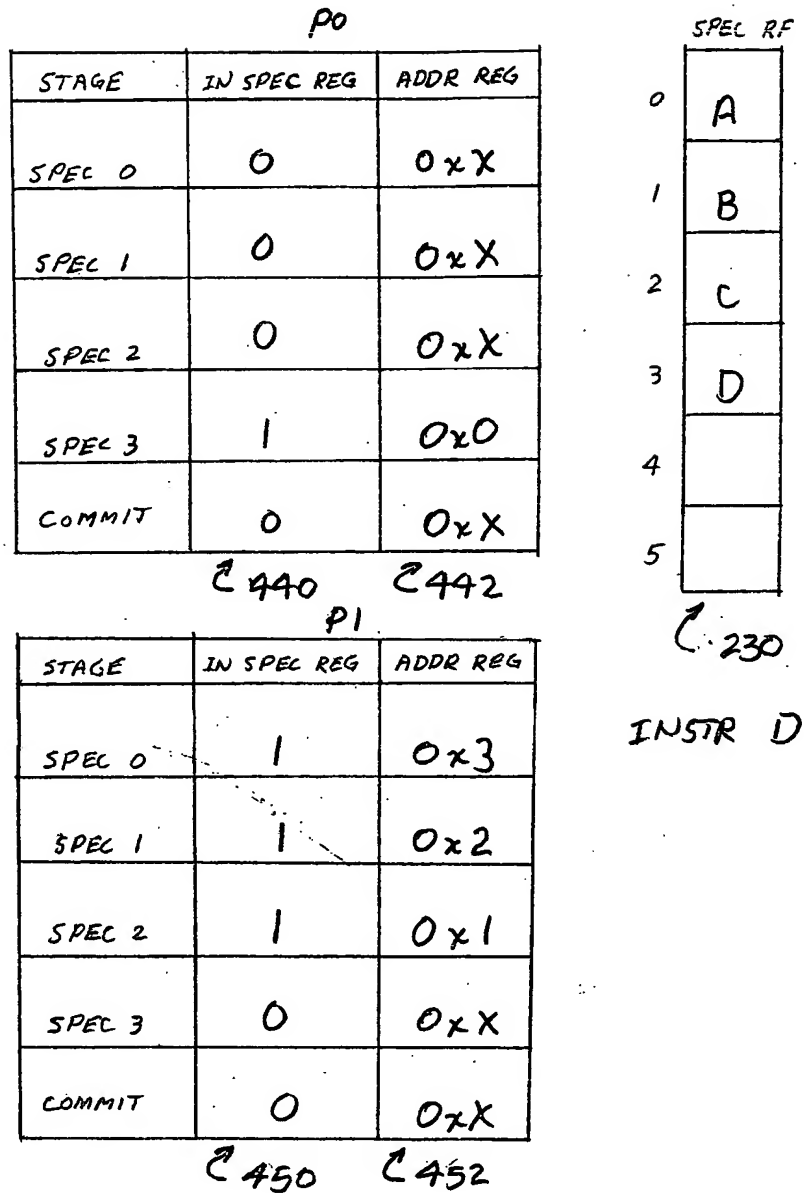


FIG. 9D

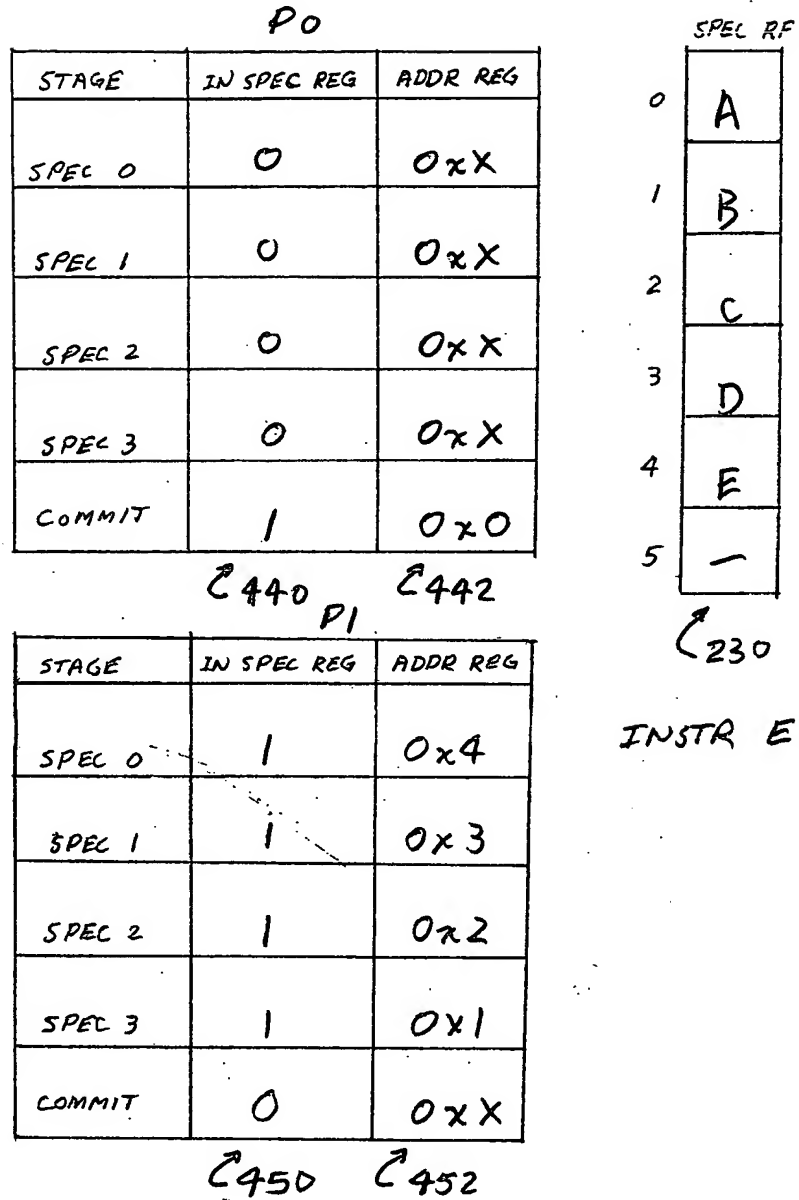


FIG. 9E



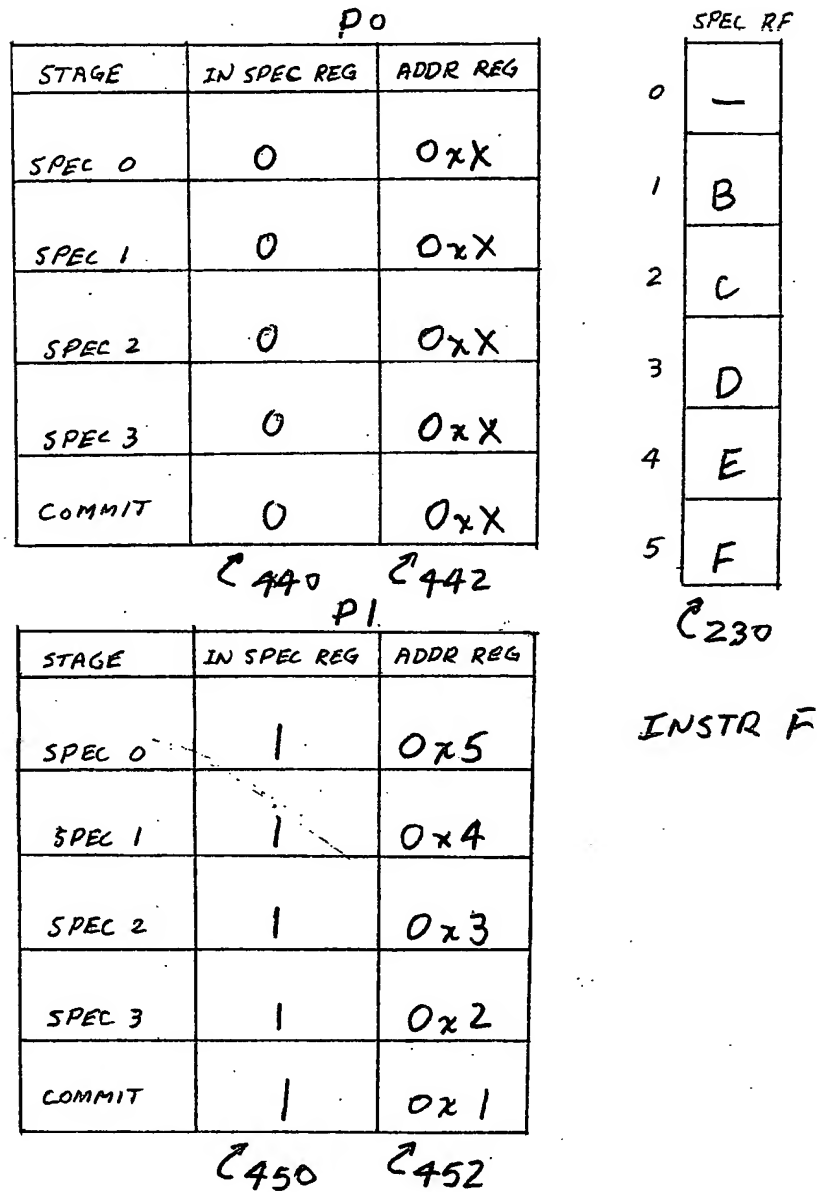


FIG. 9F